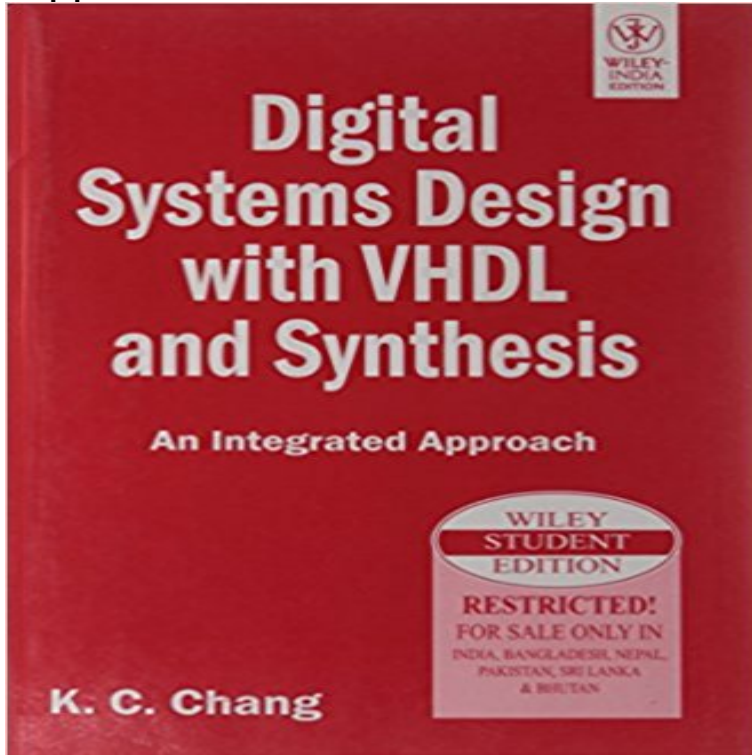


Digital Systems Design With Vhdl And Synthesis: An Integrated Approach



This book presents an integrated approach to digital design principles, processes, and implementations to help the reader design increasingly complex systems within shorter design cycles. It also introduces digital design concepts, vhdl coding, vhdl simulation, synthesis commands, and strategies together. vhdl and digital circuit primitives vhdl simulation and synthesis environment and design process basic combinational circuits basic binary arithmetic circuits basic sequential circuits registers clock and reset circuits dual-port ram, fifo, and dram modeling a design case study: finite impulse response filter asic design a design case study: a microprogram controller design error detection and correction fixed-point multiplication fixed-point division floating-point arithmetic

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